

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

3/18/03 #14/Amd+ D

Application Serial No. Filing Date Inventor Assignee Group Art Unit	$R_{\mathcal{N}}$	09/885,393
Filing Date		June 19, 2001
Inventor	As Siva	Richard Holscher et al
Assignee	17 50	Micron Technology, Inc
Group Art Unit	1 <000	1756
Examiner		Kathleen Duda
Examiner Attorney's Docket No	100	MI22-1694
Title: Semiconductor Processing Methods		

RESPONSE TO DECEMBER 13, 2002 FINAL OFFICE ACTION ACCOMPANYING RCE FILING

To:

Assistant Commissioner for Patents

Attn: Art Unit 1756

Washington, D.C. 20231

From:

Robert C. Hyta (Tel. 509-624-4276; Fax 509-838-3424)

Wells St. John P.S.

601 W. First Avenue, Suite 1300 Spokane, WA 99201-3828

AMENDMENTS

Sir:

In response to the Office action of December 13, 2003, Applicant amends and remarks as follows in accordance with proposed 37 C.F.R. § 1.121. <u>Underlines</u> indicate insertions and strikeouts indicate deletions.

There are no Amendments to the Specification in this paper.

Amendments to the Claims are reflected in the listing of the claims which begins on page-2-of-this-paper.

R marks begin on page 8 of this paper.

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